PTO/SB/81A (12-08)

Relaxed SiGe Platform for High Speed

CMOS Electronics and High Speed

PATENT - POWER OF ATTORNEY OR

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3/10/2009

Fitzgerald

Patent Number

First Named Inventor

Issue Date

Title

| CHANGE OF CORRESPONDENCE ADDRES | Analog Circuits |
|---|---|
| | Attorney Docket Number TSM09-033C1US-AW |
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| am the: Applicant/Inventor Apsigned of record of the entire interest. See 37 CFR 3.71. Statement under 37 CFR 3.73(b) (Form PTO/S/B499) submitted herewith or filed on | |
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